IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/733,899   Confirmation No.: 8459

In re application of: Hsein-Pi Chuang

Filed: December 11, 2003

Technology Center/Art Unit: 2125
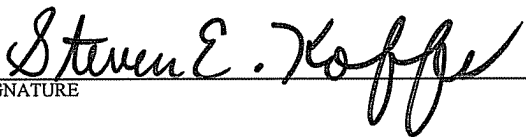
Examiner: J. Gandhi

Docket No.: N1085-00140 (TSMC2002-1188)

Customer No.: **08933**

Title: METHOD AND DATABASE
STRUCTURE FOR MANAGING
TECHNOLOGY FILES FOR
SEMICONDUCTOR MANUFACTURING
OPERATIONS

I CERTIFY THAT THIS COMMUNICATION IS BEING
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P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Sir:

Responsive to the Notice of Allowability dated **February 23, 2006**, please enter the following remarks:

REMARKS

The examiner's statement recites, "As to claims 8-12, patentability resides in a component based semiconductor fabrication process information management system for the method of claims 1-7 and 13-18." Applicant notes that claims 8-12 are not dependent on any of claims 1-7

or 13-18, and do not contain all of the limitations thereof. Therefore patentability of claims 8-12 does not rely on any features of claims 1-7 or 13-18 that are not present in claims 8-12, respectively.

In view of the foregoing amendments and remarks, Applicant submits that this application is in condition for allowance. Early notification to that effect is respectfully requested.

The Assistant Commissioner for Patents is hereby authorized to charge any additional fees or credit any excess payment that may be associated with this communication to deposit account **04-1679.**

Respectfully submitted,

Dated: _May 22, 2006_

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